

What is claimed is:

1. A method comprising:

generating a test module to produce a test result by performing a test on the sequence of instructions;

5 in the sequence of instructions, replacing a first instruction comprising a target address with a second instruction having an instruction address in the sequence, the second instruction to transfer control to the test module; and storing the target address in an encrypted table, the test module to locate the target address in the table and to transfer control to the target address if the test  
10 result indicates the sequence of instructions is to proceed.

2. The method of claim 1 further comprising

compacting the sequence of instructions to eliminate a hole created by replacing the first instruction with the second instruction.

3. The method of claim 1 further comprising:

corresponding the target address with the instruction address in the encrypted table.


4. The method of claim 1 further comprising:

profiling the sequence of instructions to identify the first instruction as an instruction to replace.

5. A device comprising:

a processor;

a machine-readable storage medium coupled to the processor by way of a bus, the  
5 storage medium storing a sequence of instructions which, when executed by the processor, cause the data processing device to generating a test module to produce a test result by performing a test on the sequence of instructions;

10  in the sequence of instructions, replace a first instruction comprising a target address with a second instruction having an instruction address in the sequence, the second instruction to transfer control to the test module; and store the target address in an encrypted table, the test module to locate the target address in the table and to transfer control to the target address if the test result indicates the sequence of instructions is to proceed.

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6. The device of claim 5 in which the sequence of instructions, when executed by the processor, further cause the device to:

5 compact the sequence of instructions to eliminate a hole created by replacing the first instruction with the second instruction.

7. The device of claim 5 in which the sequence of instructions, when executed by the processor, further cause the device to:

corresponding the target address with the instruction address in the encrypted table.

8. The device of claim 5 in which the sequence of instructions, when executed by the processor, further cause the device to:

5 profile the sequence of instructions to identify the first instruction as an instruction to replace.

9. An article comprising:

a machine-readable medium having stored thereon a sequence of instructions which, when executed by a data processing device, cause the data processing device to:

5 generating a test module to produce a test result by performing a test on the sequence of instructions;

in the sequence of instructions, replace a first instruction comprising a target address with a second instruction having an instruction address in the sequence, the second instruction to transfer control to the test module; and

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store the target address in an encrypted table, the test module to locate the target address in the table and to transfer control to the target address if the test result indicates the sequence of instructions is to proceed.

10. The article of claim 9 in which the sequence of instructions, when executed by a data processing device, further cause the data processing device to:

compact the sequence of instructions to eliminate a hole created by replacing the first instruction with the second instruction.

11. The article of claim 9 in which the sequence of instructions, when executed by a data processing device, further cause the data processing device to:

correspond the target address with the instruction address in the encrypted table.

12. The article of claim 9 in which the sequence of instructions, when executed by a data processing device, further cause the data processing device to:

profile the sequence of instructions to identify the first instruction as an instruction to replace.

13. An article comprising:

a machine-readable medium having stored thereon:

a sequence of instructions which, when executed by a data processing device, cause the data processing device to:

transfer control to a test module when a second instruction having an instruction address in the sequence is executed by the data processing device, the second instruction replacing a first instruction comprising a target address;

a test module, the test module comprising

a table comprising a target address of the replaced first instruction; and

test instructions to produce a test result by performing a test on the sequence of instructions, the test module to locate the target address in the table and to transfer control to the target address if the test result indicates the sequence of instructions is to proceed.

*Sub B3*  
~~14. The article of claim 13 in which the sequence of instructions further comprises instructions to load the test module.~~

*dc1*  
~~15. The article of claim 13 in which the test module further comprises instructions to set an exception handler to transfer control to the test instructions when the second instruction is executed by the data processing device.~~

*Sub B3*  
~~16. The article of claim 14 in which the test module further comprises instructions moved from the sequence of instructions to make room for the instructions to load the test module.~~